

FIG. 1

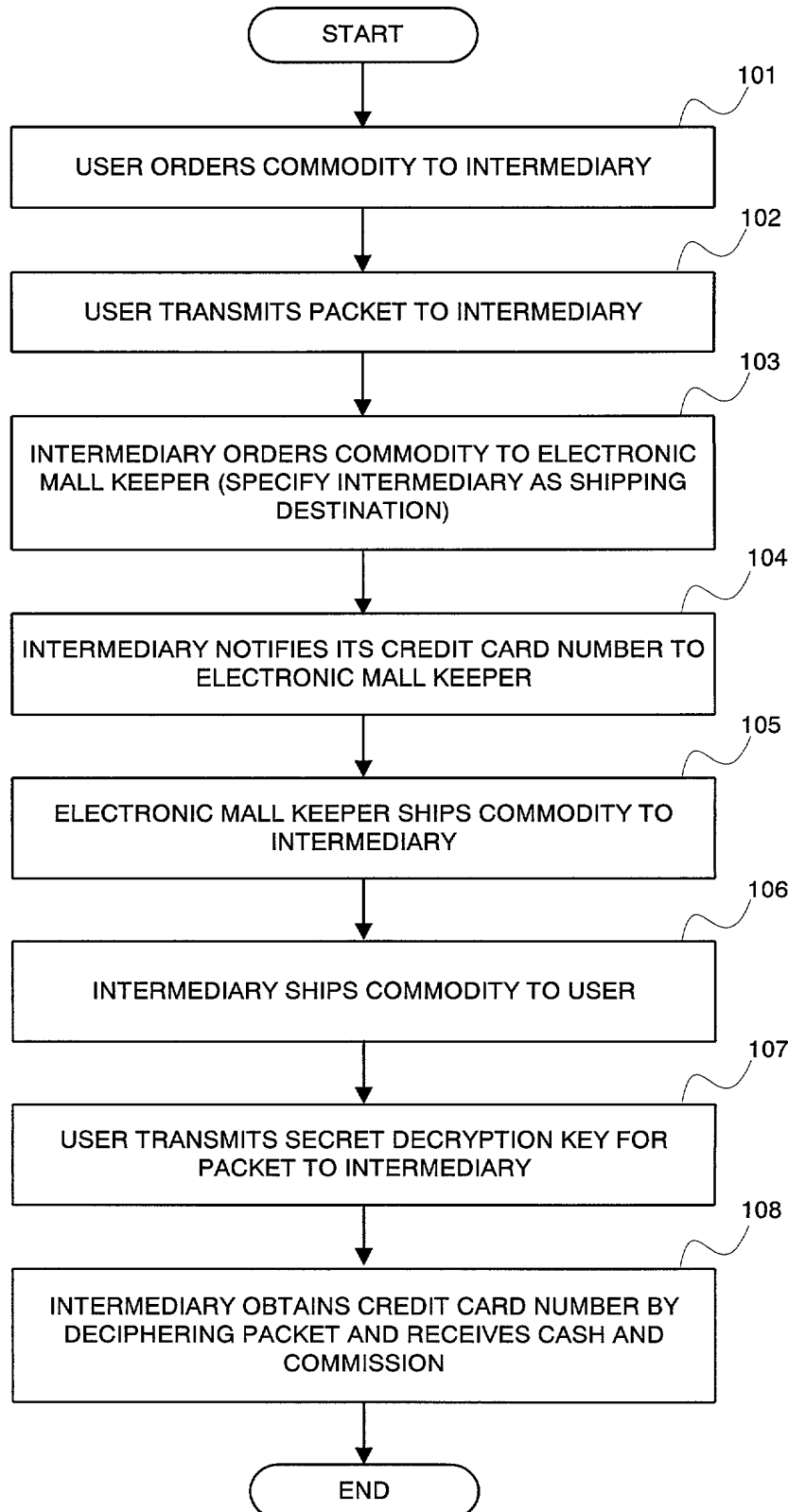


FIG. 2

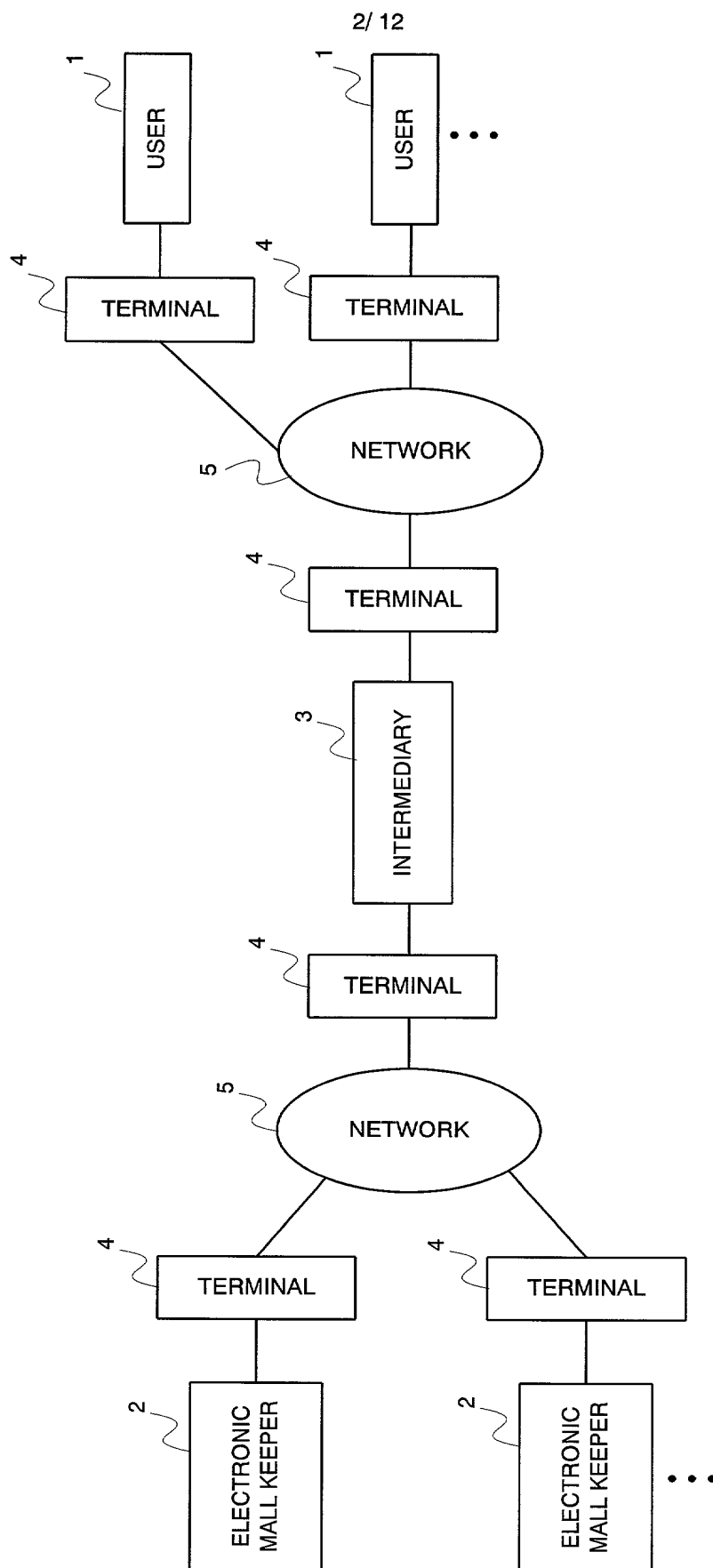


FIG. 3

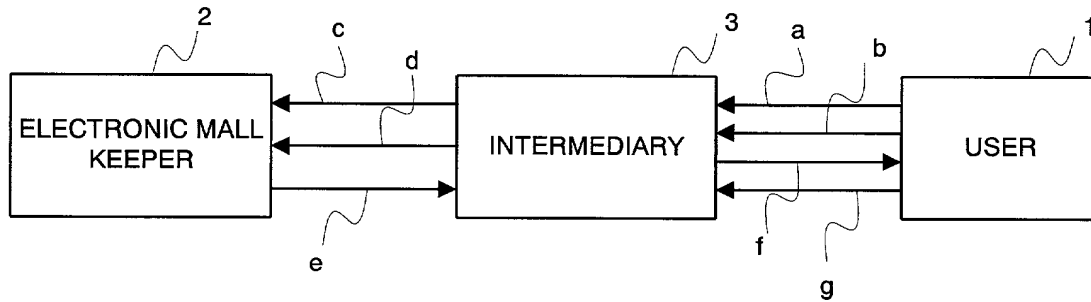


FIG. 4

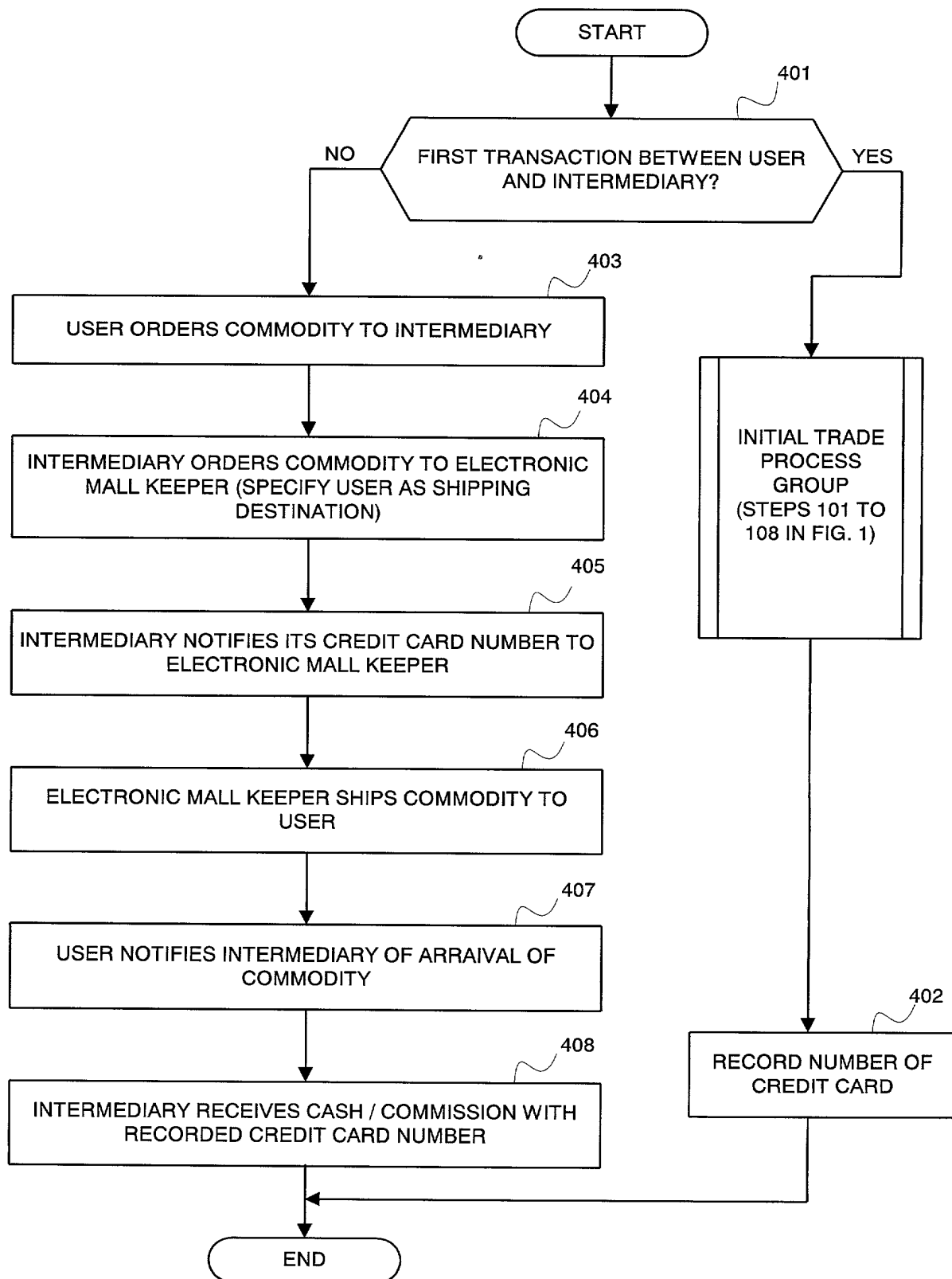


FIG. 5

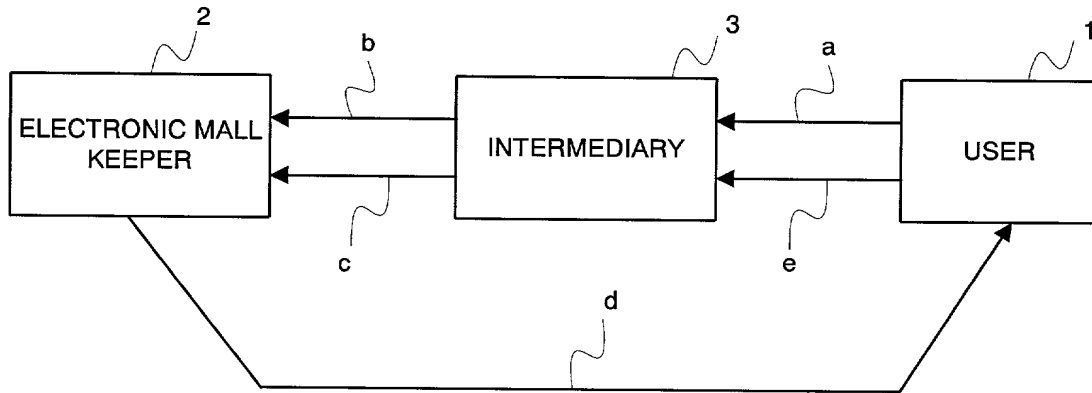


FIG. 6

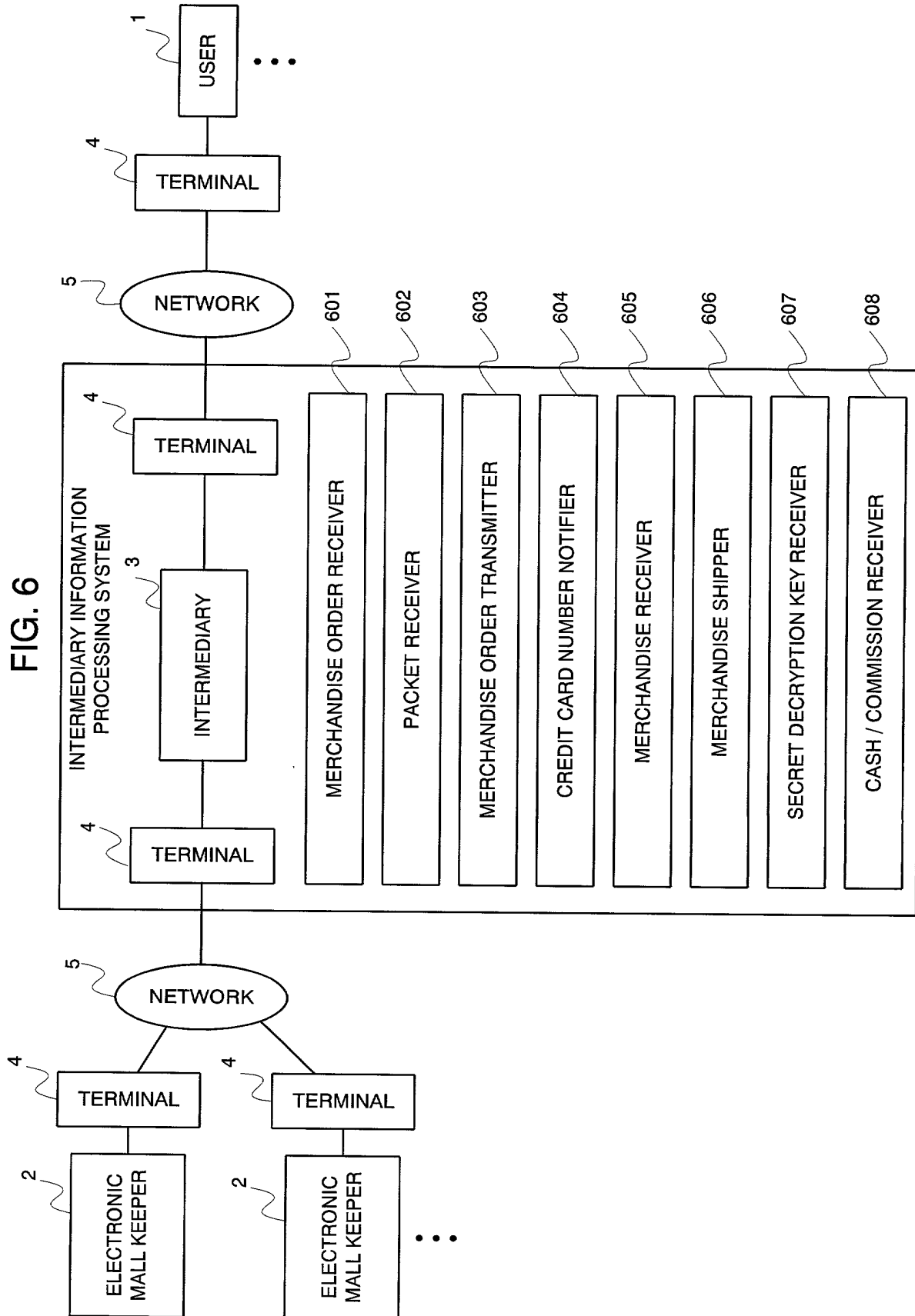
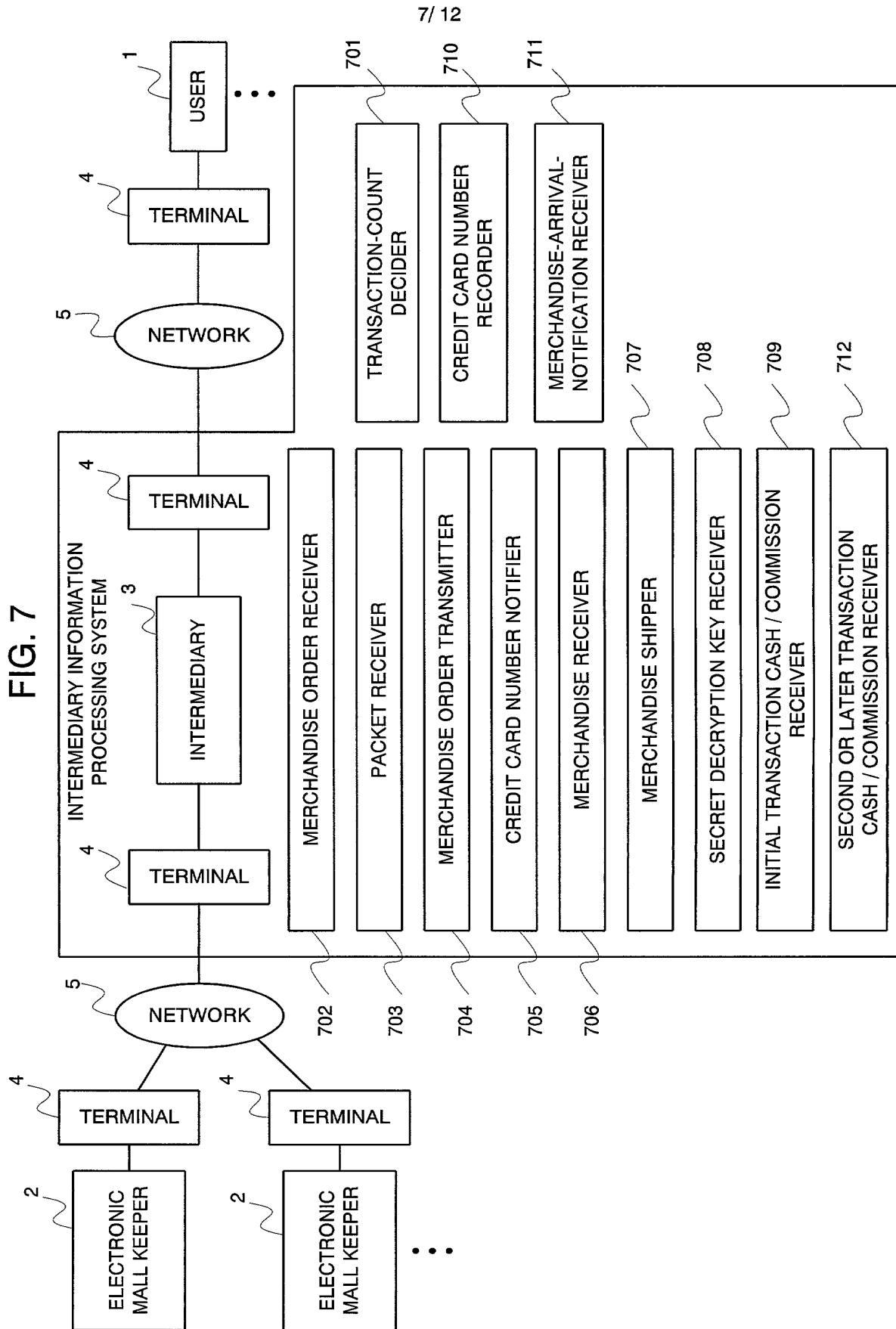


FIG. 7



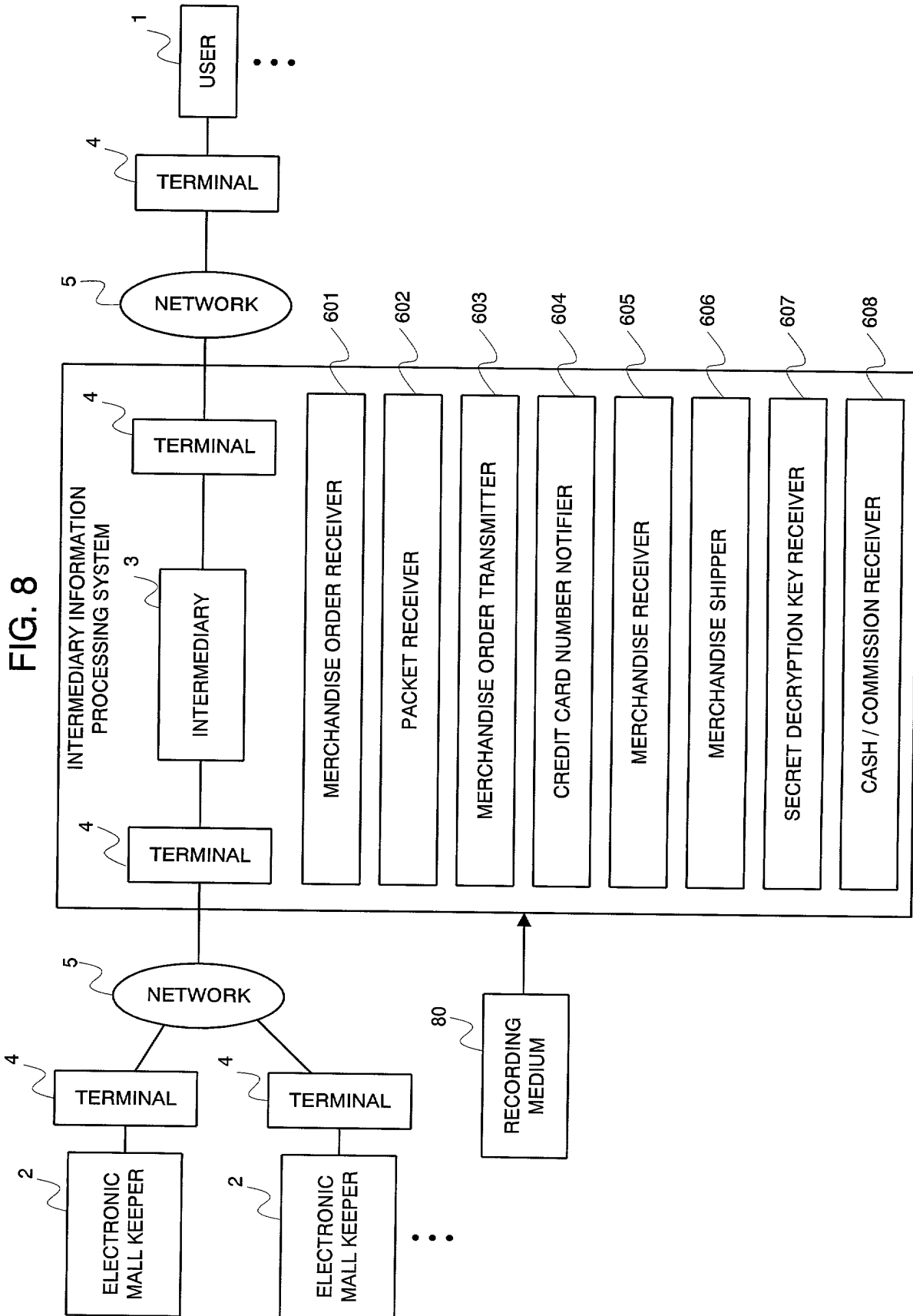
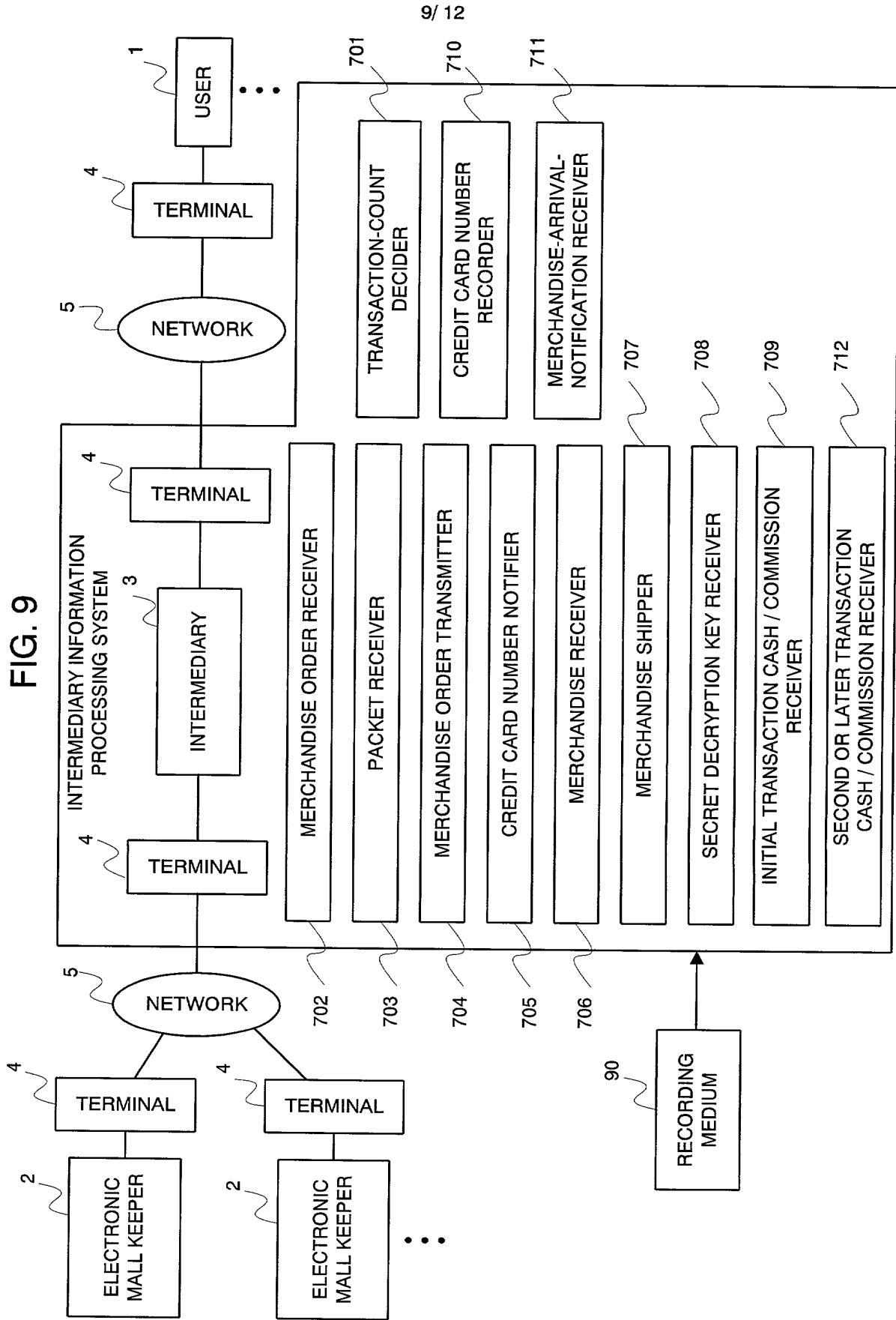




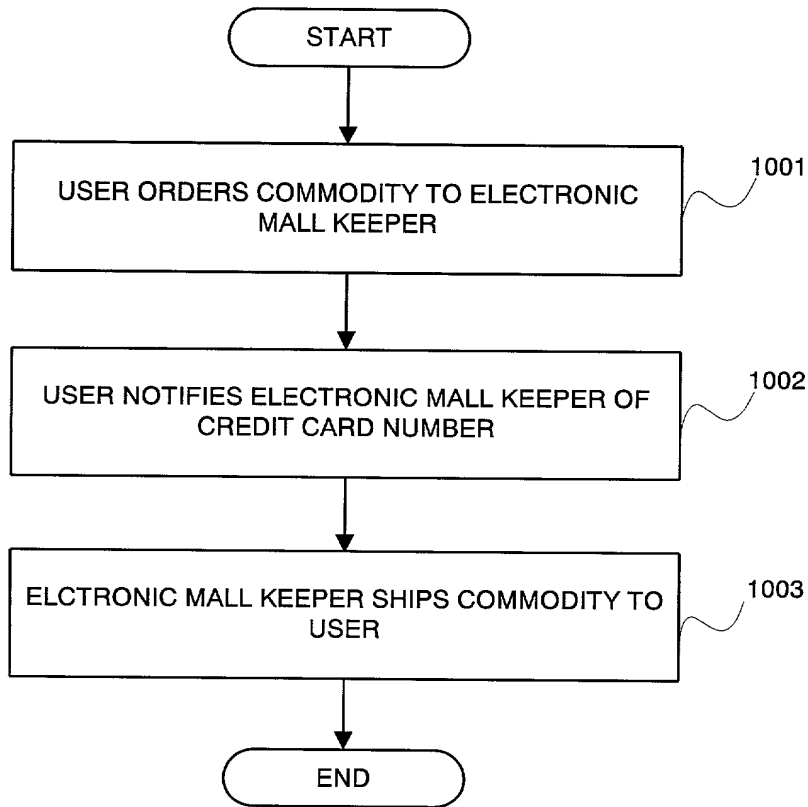
FIG. 9 is a block diagram of an intermediary information processing system.

FIG. 9

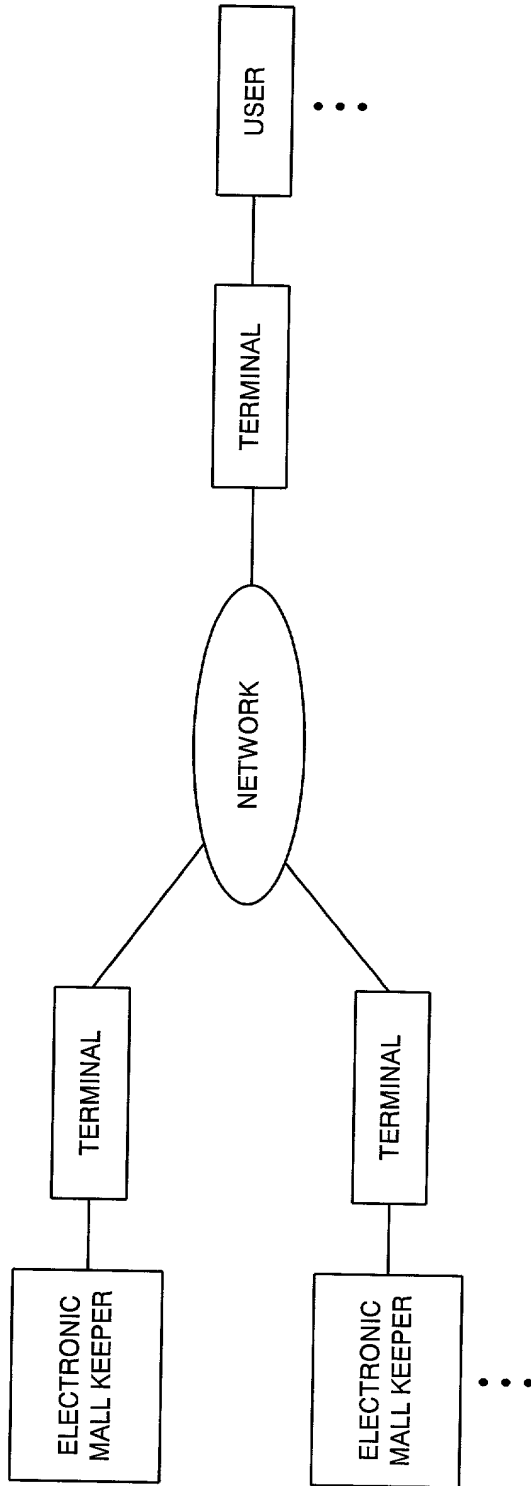


## PRIOR ART

FIG. 10



PRIOR ART  
FIG. 11



PRIOR ART  
FIG. 12

